Ref #	Hits	Search Query 9 MM	DBS ON M	Default Operator	Plurals	Time Stamp
S20 2	948	((inter near3 (shel\$4 rack\$4)) inter\$1shel\$4 inter\$1rack\$4)((multi near3 (shel\$4 rack\$4)) multi\$1shel\$4 multi\$1rack\$4) and (back\$1plane (back near3 plane\$1)) and (((daisy\$4 near3 chain\$4) daisy\$4chanin\$3) near3 switch\$4)	inter\$1shel\$4 inter\$1rack\$4)((multi near3 (shel\$4 rack\$4)) multi\$1shel\$4 multi\$1rack\$4) and (back\$1plane (back near3 plane\$1)) and (((daisy\$4 near3 chain\$4)		OFF	2006/02/15 10:19
S20 0	959	((inter near3 (shel\$4 rack\$4)) inter\$1shel\$4 inter\$1rack\$4)((multi near3 (shel\$4 rack\$4)) multi\$1shel\$4 multi\$1rack\$4) and (back\$1plane (back near3 plane\$1)) and ((daisy\$4 near3 chain\$4) daisy\$4chanin\$3)	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 10:19
S20 4	114	(((inter near3 (shel\$4 rack\$4)) inter\$1shel\$4 inter\$1rack\$4)((multi near3 (shel\$4 rack\$4)) multi\$1shel\$4 multi\$1rack\$4) same ((daisy\$4 near3 chain\$4) daisy\$4chanin\$3)) and (back\$1plane (back near3 plane\$1))	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 11:41
S20 7	12944	(four "4") near3 (switch\$4 cross\$1connect\$1) near3 (card\$1 unit\$1 device\$1 module\$1)	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 11:43
S20 6	28202	(sixteen "16") near3 (line\$1 interface\$1 input output\$1 ingress\$4 egress\$4) near3 (card\$1 unit\$1 device\$1 module\$1)	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 11:44
S20 9	28	S206 and S207 and S208	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 11:45
S21 1	283	370/353.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 13:39
S21 2	253	370/354.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 13:55
S21 3	273	370/355.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 15:09
S20 8	480	(sixteen "16") near3 port\$1 near3 (line\$1 interface\$1 input output\$1 ingress\$4 egress\$4) near3 (card\$1 unit\$1 device\$1 module\$1)	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 15:24

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S21 7	33433	(sixteen) near3 (input\$1 ingresss\$1 output\$1 engress\$4) port\$1 near3 (line\$1 interface\$1 input output\$1 ingress\$4 egress\$4) near3 (card\$1 unit\$1 device\$1 module\$1)	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 15:27
S21 8	135114	(sixteen "16") near3 (output\$1 engress\$4) port\$1 near3 (line\$1 interface\$1 input output\$1 ingress\$4 egress\$4) near3 (card\$1 unit\$1 device\$1 module\$1)	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 15:28
S21 6	183049	(sixteen "16") near3 (input\$1 ingresss\$1 output\$1 engress\$4) port\$1 near3 (line\$1 interface\$1 input output\$1 ingress\$4 egress\$4) near3 (card\$1 unit\$1 device\$1 module\$1)	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 15:28
S22 2	12944	(four "4") near3 (switch\$4 cross\$1connect\$1) near3 (card\$1 unit\$1 device\$1 module\$1)	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 15:29
S22 1	28202	(sixteen "16") near3 (line\$1 interface\$1 input output\$1 ingress\$4 egress\$4) near3 (card\$1 unit\$1 device\$1 module\$1)	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 15:29
S22 0	59349	S218 and S219	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 15:29
S22 5	28	S221 and S222 and S223 and S220	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 15:30
S22 4	28	S221 and S222 and S223	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 15:30
S22 9	325	370/388.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 15:45
S22 8	48	(sixteen) near3 port\$1 near3 (line\$1 interface\$1 input output\$1 ingress\$4 egress\$4) near3 (card\$1 unit\$1 device\$1 module\$1)	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 15:45
S22 3	480	(sixteen "16") near3 port\$1 near3 (line\$1 interface\$1 input output\$1 ingress\$4 egress\$4) near3 (card\$1 unit\$1 device\$1 module\$1)	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 15:45
S21 5	179	370/366.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 15:51
S23 2	329	370/413.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 16:19

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S23 1	481	370/343.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 16:19
S23 3	255	370/422.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 16:20
S23 5	64	((line\$1 interface\$1 input output\$1 ingress\$4 egress\$4) near3 (card\$1 unit\$1 device\$1 module\$1)) same S234	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 16:21
S21 9	107284	(sixteen "16") near3 (input\$1 ingresss\$1) port\$1 near3 (line\$1 interface\$1 input output\$1 ingress\$4 egress\$4) near3 (card\$1 unit\$1 device\$1 module\$1)	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 16:21
S23 8	16	sram same dram same (cpu processor\$4) same asic same phy	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 16:32
S23 4	493	sram same dram same (cpu processor\$4) same asic	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 16:32
S24 1	1509	709/250.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 17:06
S24 2	52	sram and dram and (cpu processor\$4) and asic and S241	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 17:07
S23 9	182	sram and dram and (cpu processor\$4) and asic and phy	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 17:07
S23 7	934	370/463.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 17:12
S24 7	136	370/414.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 17:23
S24 5	5780	(shel\$4 rack\$4 chassi\$4) and (back\$1plane (back near3 plane\$1)) and (packet\$1 cell\$1 frame\$1)	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 17:23
S24 4	522	370/419.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 17:23
S24 8	114	370/415.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 17:47
S21 4	410	370/359.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 18:27

S24 9	329	370/413.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/16 09:04
S25 3	255	370/422.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/16 09:24
S25 4	82	370/423.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/16 09:28
S25 6	523	370/419.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/16 09:29
S25 5	523	370/419.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/16 09:29
S24 6	34	S244 and S245	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/16 09:29
S25 7	523	S256 and S255	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/16 09:42
S25 8	136	370/414.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/16 09:44
S25 9	114	370/415.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/16 09:54
S26 0	331	370/413.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/16 10:28
S26 2	5790	(shel\$4 rack\$4 chassi\$4) and (back\$1plane (back near3 plane\$1)) and (packet\$1 cell\$1 frame\$1)	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/16 10:35
S26 3	34	S257 and S262	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/16 10:36
S1	6	((inter near3 (shel\$4 rack\$4)) inter\$1shel\$4 inter\$1rack\$4).ab. and (back\$1plane (back near3 plane\$1))	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/16 10:36
S26 1	4129	370/352.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/16 10:37
S26 4	82	S261 and S262	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/16 10:47

S26 5	1060	370/535.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/16 10:48
S26 8	35	S267 and S262	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/16 11:55
S26 6	34	S265 and S262	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/16 11:55
S26 7	1786	370/465.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/16 13:15
L2	5790	(shel\$4 rack\$4 chassi\$4) and (back\$1plane (back near3 plane\$1)) and (packet\$1 cell\$1 frame\$1)	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/16 13:16
L7	3	6 and 2	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/16 13:28
L6	2265	707/2.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/16 13:28
L5	11	4 and 2	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/16 13:28
L4	4642	707/1.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/16 13:28
L3	65	1 and 2	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/16 13:28
L1	1772	370/466.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/16 13:28
L13	23	12 and 2	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/16 13:29
L12	2014	711/114.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/16 13:29
L11	10	10 and 2	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/16 13:29
L10	1118	711/113.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/16 13:29

L9	5	8 and 2	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/16 13:29
L8	3980	707/100.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/16 13:29

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Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S20 2	948	((inter near3 (shel\$4 rack\$4)) inter\$1shel\$4 inter\$1rack\$4)((multi near3 (shel\$4 rack\$4)) multi\$1shel\$4 multi\$1rack\$4) and (back\$1plane (back near3 plane\$1)) and (((daisy\$4 near3 chain\$4) daisy\$4chanin\$3) near3 switch\$4)	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 10:19
S20 0	959	((inter near3 (shel\$4 rack\$4)) inter\$1shel\$4 inter\$1rack\$4)((multi near3 (shel\$4 rack\$4)) multi\$1shel\$4 multi\$1rack\$4) and (back\$1plane (back near3 plane\$1)) and ((daisy\$4 near3 chain\$4) daisy\$4chanin\$3)	er\$1shel\$4 inter\$1rack\$4)((multi USPAT; EPO lti\$1shel\$4 multi\$1rack\$4) and ck\$1plane (back near3 plane\$1)) l ((daisy\$4 near3 chain\$4)		2006/02/15 10:19	
\$20 4	114	(((inter near3 (shel\$4 rack\$4)) inter\$1shel\$4 inter\$1rack\$4)((multi near3 (shel\$4 rack\$4)) multi\$1shel\$4 multi\$1rack\$4) same ((daisy\$4 near3 chain\$4) daisy\$4chanin\$3)) and (back\$1plane (back near3 plane\$1))	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 11:41
S20 7	12944	(four "4") near3 (switch\$4 cross\$1connect\$1) near3 (card\$1 unit\$1 device\$1 module\$1)	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 11:43
S20 6	28202	(sixteen "16") near3 (line\$1 interface\$1 input output\$1 ingress\$4 egress\$4) near3 (card\$1 unit\$1 device\$1 module\$1)	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 11:44
S20 9	28	S206 and S207 and S208	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 11:45
S21 1	283	370/353.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 13:39
S21 2	253	370/354.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 13:55
S21 3	273	370/355.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 15:09
S20 8	480	(sixteen "16") near3 port\$1 near3 (line\$1 interface\$1 input output\$1 ingress\$4 egress\$4) near3 (card\$1 unit\$1 device\$1 module\$1)	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 15:24

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S21 7	33433	(sixteen) near3 (input\$1 ingresss\$1 output\$1 engress\$4) port\$1 near3 (line\$1 interface\$1 input output\$1 ingress\$4 egress\$4) near3 (card\$1 unit\$1 device\$1 module\$1)	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 15:27
S21 8	135114	(sixteen "16") near3 (output\$1 engress\$4) port\$1 near3 (line\$1 interface\$1 input output\$1 ingress\$4 egress\$4) near3 (card\$1 unit\$1 device\$1 module\$1)	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 15:28
S21 6	183049	(sixteen "16") near3 (input\$1 ingresss\$1 output\$1 engress\$4) port\$1 near3 (line\$1 interface\$1 input output\$1 ingress\$4 egress\$4) near3 (card\$1 unit\$1 device\$1 module\$1)	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 15:28
S22 2	12944	(four "4") near3 (switch\$4 cross\$1connect\$1) near3 (card\$1 unit\$1 device\$1 module\$1)	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 15:29
S22 1	28202	(sixteen "16") near3 (line\$1 interface\$1 input output\$1 ingress\$4 egress\$4) near3 (card\$1 unit\$1 device\$1 module\$1)	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 15:29
S22 0	59349	S218 and S219	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 15:29
S22 5	28	S221 and S222 and S223 and S220	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 15:30
S22 4	28	S221 and S222 and S223	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 15:30
S22 9	325	370/388.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 15:45
S22 8	48	(sixteen) near3 port\$1 near3 (line\$1 interface\$1 input output\$1 ingress\$4 egress\$4) near3 (card\$1 unit\$1 device\$1 module\$1)	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 15:45
S22 3	480	(sixteen "16") near3 port\$1 near3 (line\$1 interface\$1 input output\$1 ingress\$4 egress\$4) near3 (card\$1 unit\$1 device\$1 module\$1)	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 15:45
S21 5	179	370/366.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 15:51
S23 2	329	370/413.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 16:19

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S23 1	481	370/343.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 16:19
S23 3	255	370/422.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 16:20
S23 5	64	((line\$1 interface\$1 input output\$1 ingress\$4 egress\$4) near3 (card\$1 unit\$1 device\$1 module\$1)) same S234	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 16:21
S21 9	107284	(sixteen "16") near3 (input\$1 ingresss\$1) port\$1 near3 (line\$1 interface\$1 input output\$1 ingress\$4 egress\$4) near3 (card\$1 unit\$1 device\$1 module\$1)	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 16:21
S23 8	16	sram same dram same (cpu processor\$4) same asic same phy	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 16:32
S23 4	493	sram same dram same (cpu processor\$4) same asic	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 16:32
S24 1	1509	709/250.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 17:06
S24 2	52	sram and dram and (cpu processor\$4) and asic and S241	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 17:07
S23 9	182	sram and dram and (cpu processor\$4) and asic and phy	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 17:07
S23 7	934	370/463.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 17:12
S24 7	136	370/414.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 17:23
S24 5	5780	(shel\$4 rack\$4 chassi\$4) and (back\$1plane (back near3 plane\$1)) and (packet\$1 cell\$1 frame\$1)	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 17:23
S24 4	522	370/419.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 17:23
S24 8	114	370/415.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 17:47
S21 4	410	370/359.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/15 18:27

S24 9	329	370/413.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/16 09:04
S25 3	255	370/422.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/16 09:24
S25 4	82	370/423.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/16 09:28
S25 6	523	370/419.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/16 09:29
S25 5	523	370/419.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/16 09:29
S24 6	34	S244 and S245	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/16 09:29
S25 7	523	S256 and S255	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/16 09:42
S25 8	136	370/414.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/16 09:44
S25 9	114	370/415.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/16 09:54
S26 0	331	370/413.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/16 10:28
S26 2	5790	(shel\$4 rack\$4 chassi\$4) and (back\$1plane (back near3 plane\$1)) and (packet\$1 cell\$1 frame\$1)	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/16 10:35
S26 3	34	S257 and S262	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/16 10:36
S1	6	((inter near3 (shel\$4 rack\$4)) inter\$1shel\$4 inter\$1rack\$4).ab. and (back\$1plane (back near3 plane\$1))	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/16 10:36
S26 1	4129	370/352.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/16 10:37
S26 4	82	S261 and S262	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/16 10:47

S26 5	1060	370/535.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/16 10:48
S26 8	35	S267 and S262	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/16 11:55
S26 6	34	S265 and S262	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/16 11:55
S26 7	1786	370/465.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/16 13:15
L2	5790	(shel\$4 rack\$4 chassi\$4) and (back\$1plane (back near3 plane\$1)) and (packet\$1 cell\$1 frame\$1)	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/16 13:16
L7	3	6 and 2	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/16 13:28
L6	2265	707/2.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/16 13:28
L5	11	4 and 2	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/16 13:28
L4	4642	707/1.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/16 13:28
L3	65	1 and 2	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/16 13:28
L1	1772	370/466.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/16 13:28
L13	23	12 and 2	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/16 13:29
L12	2014	711/114.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/16 13:29
L11	10	10 and 2	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/16 13:29
L10	1118	711/113.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/16 13:29

L9	5	8 and 2	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/16 13:29
L8	3980	707/100.ccls.	US-PGPUB; USPAT; EPO	OR	OFF	2006/02/16 13:29